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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/687,288	10/15/2003	Wang Yueh	42P17301	7538

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EXAMINER

CHACKO DAVIS, DABORAH

ART UNIT	PAPER NUMBER
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1756

DATE MAILED: 09/20/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/687,288

Applicant(s)

YUEH ET AL.

Examiner

Daborah Chacko-Davis

Art Unit

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 15 October 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-20 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-5, 7, and 10-14, are rejected under 35 U.S.C. 102(b) as being anticipated by U. S. Patent 5,759,739 (Takemura et al).

Takemura, in the abstract, in col 1, lines 10-13, in col 3, lines 30-37, in col 4, lines 3-14, in col 5, lines 1-24, in col 6, lines 66-67, in col 7, lines 1-30, discloses a process of patterning features on the substrate (super LSIs) by forming a photoresist layer on the substrate (integrated device to be fabricated), wherein the photoresist includes an alkali-soluble resin, and a photoacid generator (photoactive), exposing the photoresist layer to EUV (excimer radiations) such that selected portions (exposed portions) are rendered soluble in the developer (during the developing process) by the acid generated by the photoacid generator during exposure, and the unexposed portions are inhibited from being rendered soluble in the developer (claims 1, 3, 7, 10, 12). Takemura, in col 4, lines 3-6, discloses that the alkali-soluble resin is polyhydroxystyrene (claims 2, and 11). Takemura, in col 10, lines 43-47, discloses that the photoactive agent contains a phenyl group (claims 4, and 13). Takemura, in col 5, lines 20-24, discloses that the acid unstable group is a carbonyl group (claims 5, and 14).

3. Claims 1, 6-7, 10, and 15, are rejected under 35 U.S.C. 102(b) as being anticipated by U. S. Patent No. 5,358,599 (Cathey et al).

Cathey, in the abstract, in col 3, lines 36-68, and in col 4, lines 5-44, in col 6, lines 18-20, discloses a process of patterning a semiconductor device in a lithography tool, the device including a plurality of structural layers by forming a photoresist layer on the structural layers, wherein the photoresist includes a photoacid generator that prevents selected portions of the resist from being solubilized by the developer, exposing the resist to UV radiation (EUV), and said acid generator renders selected portions (unexposed non-crosslinked portion) of the resist soluble in the developer during the development step (claims 1, 7, and 10). Cathey, in col 4, lines 40-44, disclose that the resin is a poly vinyl phenol resin (claims 6, and 15).

Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claims 8-9, 16-20, are rejected under 35 U.S.C. 103(a) as being unpatentable over U. S. Patent No. 5,358,599 (Cathey et al) in view of U. S. Patent Application Publication No. 2004/0204328 (Zhang et al).

Cathey, in the abstract, in col 3, lines 36-68, in col 4, lines 5-44, in col 5, lines 1-10, and in col 6, lines 18-44, discloses a process of patterning a semiconductor device

in a lithography tool, the device including a plurality of structural layers (metal layers), patterning the photoresist layer formed on the structural layers to form a photoresist etch mask, wherein the photoresist etch mask is used to etch the exposed structural layers underlying the mask, followed by stripping the remaining photoresist mask (claims 8, 16, 18). Cathey, in col 6, lines 51-52, discloses that the patterning process is used to manufacture integrated circuits (claim 20).

The difference between the claims and Cathey is that Cathey does not disclose that the patterns formed in the device have a critical dimension of approximately 15 nm. Cathey does not disclose that the line wide roughness of the feature is less than 2 nanometers (claims 9, 17). Cathey does not disclose that the line wide roughness of a given feature is no more than 10% of the critical dimension of the particular feature (claim 19).

Zhang, in [0019], discloses that the features formed in the device have a critical dimension less than 2nm, and a line width roughness of less than 2nm, and that the line width roughness is within 8% of the critical dimension.

Therefore, it would be obvious to a skilled artisan to modify Cathey by employing the method of patterning as suggested by Zhang, because Zhang, in [0019], discloses modifying the photoresist formulation and adjusting the latent image results in a pattern of reduced roughness.

Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Daborah Chacko-Davis whose telephone number is

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(571) 272-1380. The examiner can normally be reached on M-F 9:30 - 6:00. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mark F Huff can be reached on (571) 272-1385. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

dcd


September 15, 2005.


JOHN A. MCPHERSON
PRIMARY EXAMINER